

FIGURE 1  
(PRIOR ART)

HEADER

F1	4	F2	1	F3	1
F5	2	*	N		
C1					
C1					
C1					
C1					
C2					
C3					
C5					
C5					

FIGURE 3

ADDR\_START:

	F1	F2	F3	F4	F5
X		X			X
X		X			X
	X		X		
X	X				X
X			X		X

TIME

N CYCLES

FIGURE 2

ADDR\_END:

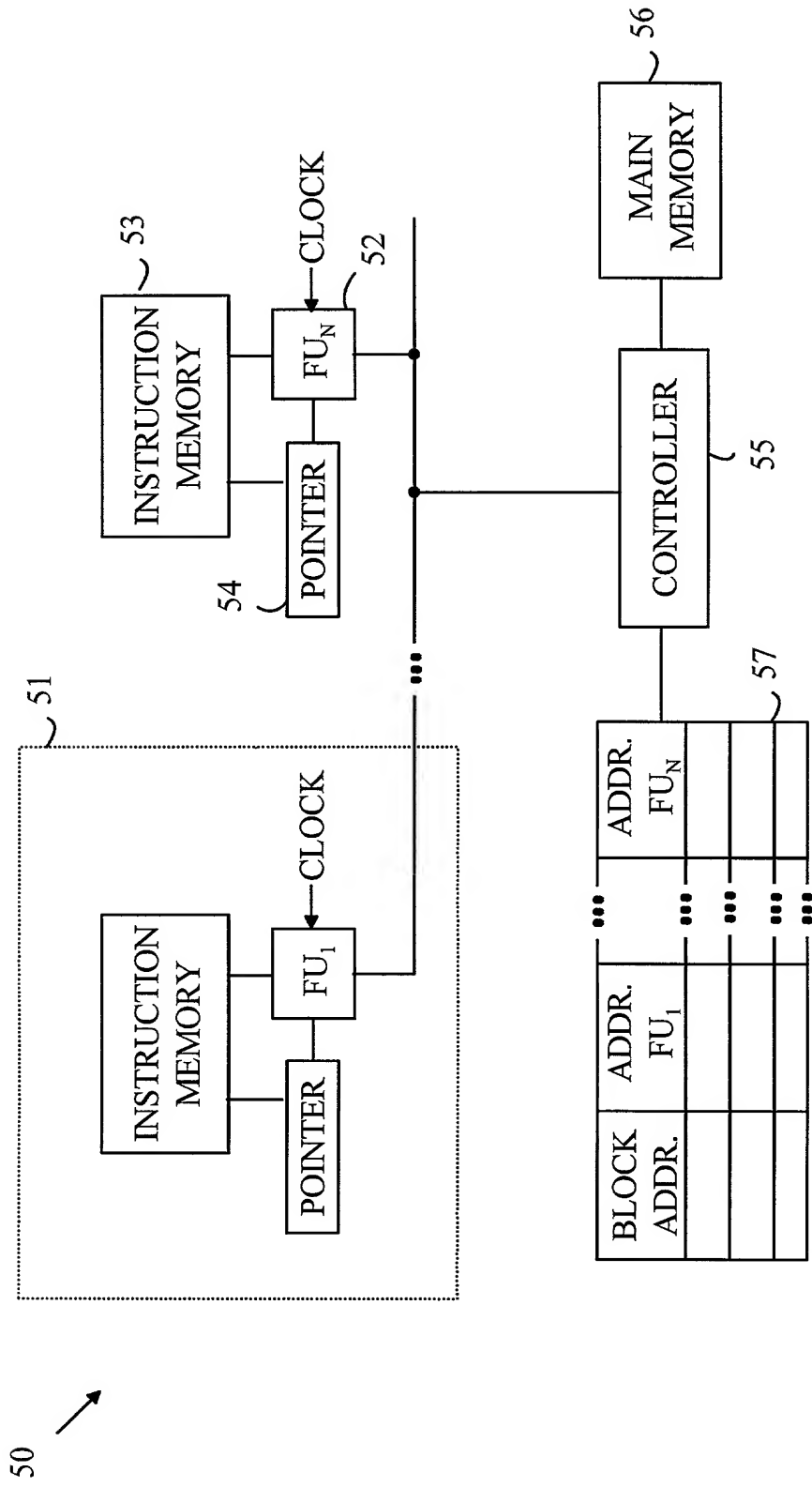


FIGURE 4

